HV Tests Performed

- Total number of boards received:
  - A876: 58 (41 tested)
  - A877 standard: 172
  - A877 MB4: 57
- 4 different tests:
  - Test 1: Min. voltage, Max. current
    - Resistive Loads
    - 7 days per board (8 boards at the same time)
  - Test 2: Max. voltage, Max. current
    - RC loads (same of Test 3)
    - 6 days per board (8 boards at the same time)
  - Test 3: Ramping cycles
    - RC loads (same of Test 2)
    - 1 day per board (8 boards at the same time)
  - Test 4: Min. voltage, Null current
    - No loads
    - 7 days per board

HV Tests Summary

- Each board needs 3 weeks to be tested
- Test 1, Test 2, Test 3 :
  - A877 standard tested: 76
  - A877 MB4 tested: 14
- Test 4:
  - A877 standard tested: 172
  - A877 MB4 tested: 30

HV Problems

- No A876 had problems
- A877: Tests with loads (#1,2,3)
  - UnV (wires cannot reach the set value): 2
  - OvV (in the working range): 1
  - Readout problems (in 1 channel iMon always 0): 2
  - In Test 1: OvC with oscillating Voltage value: 26 (14 standard, 12 MB4)
    - Problem still under study, not clear if needs hardware intervention
    - Good input: no problems during Tests 2 and 3, where boards are used in the working range (while they will never really work in the condition of Test 1, i.e. V_{Min}-i_{Max})
- A877: Test without loads (#4) (problems already repaired by CAEN)
  - 2 Calibration Errors
  - 5 OvC, discharges, noises..
  - 5 UnV
  - 3 OvV