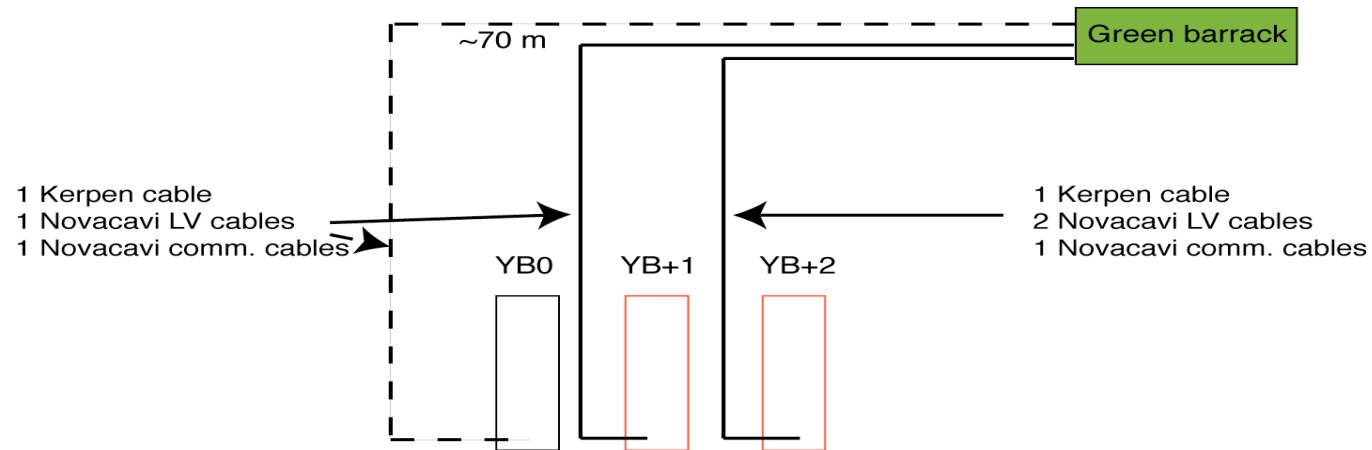


CMS WEEK 20th June 2006

DT HV/LV status and plans for MTCC

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DT Setup for MTCC



HV

Sectors 10 and 11 in YB+2 (9 DTs): 3 A876, 9 A877

Sector 10 in YB+1 (5 DTs): 2 A876, 5 A877

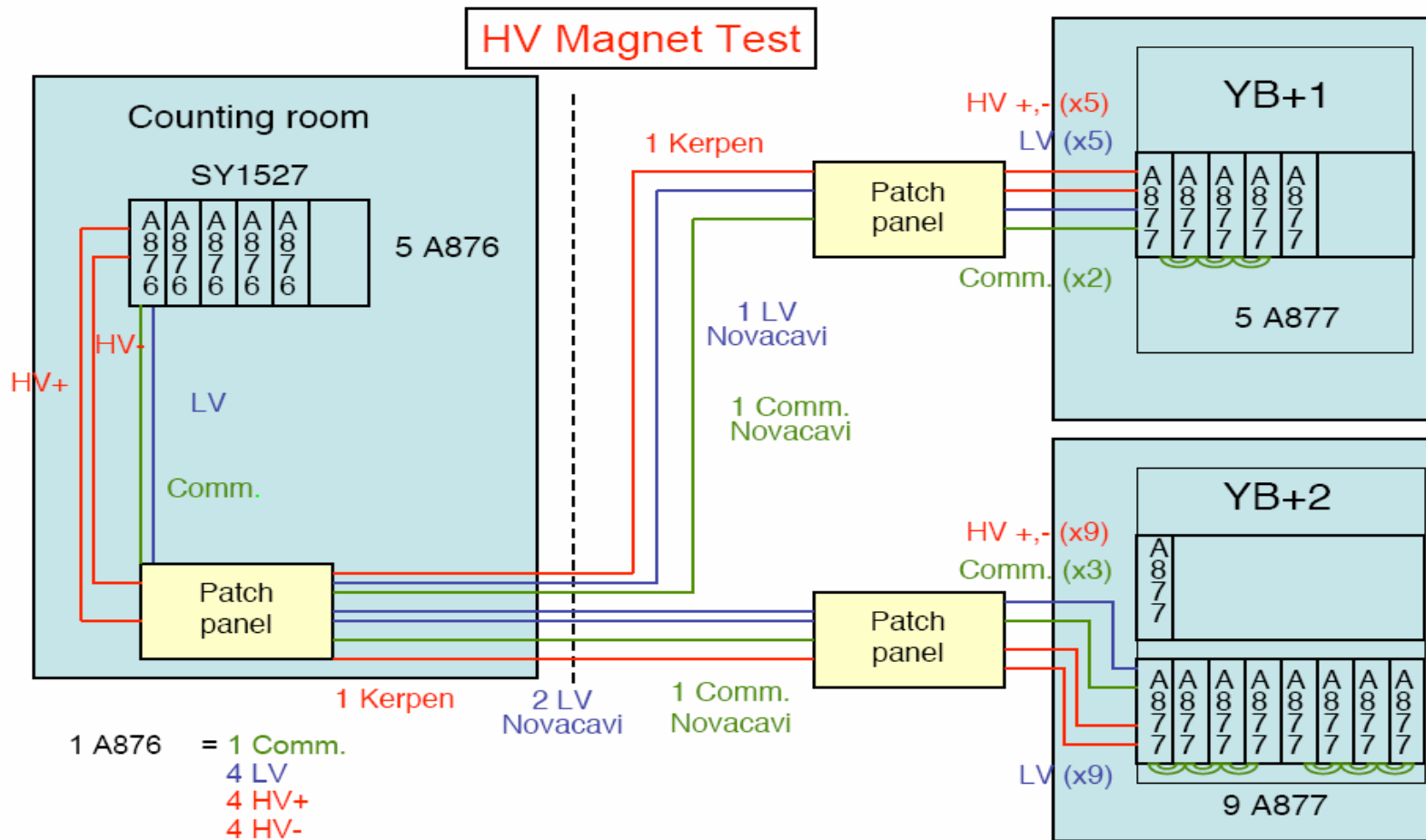
Sector 10 in YB0 (5 DTs): 2 A876, 5 A877

We will have 3 DT sectors in 2 wheels (YB+2 and YB+1) ~ total of 14 DTs.

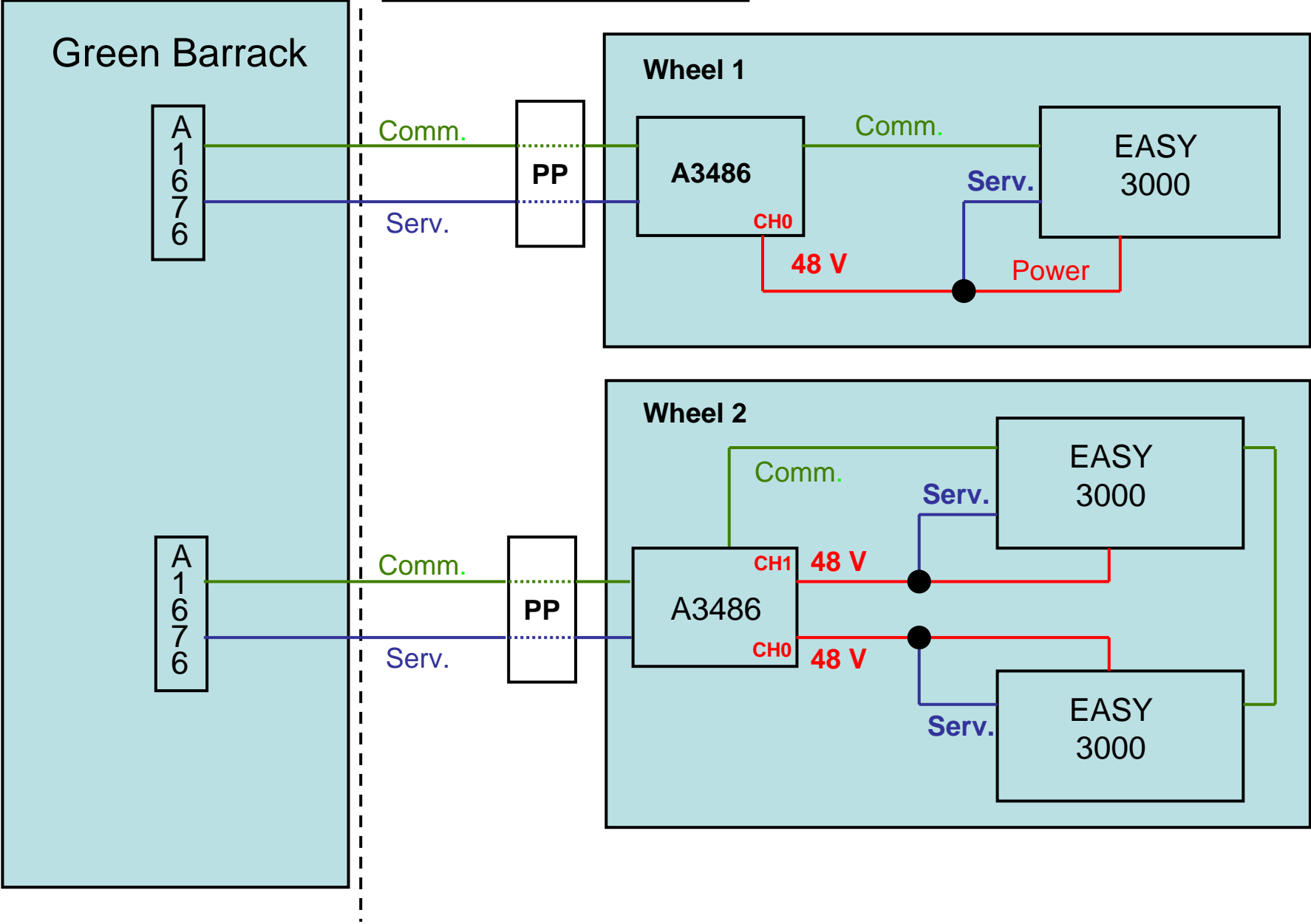
HV needs: 1 SY1527, 5 A876 and 14 A877.

LV Needs: 2 A3486, 2 A1676A, 5 A3009, 8 A3050, 2 A3100.

HV Hardware



LV Magnet Test



Hardware status

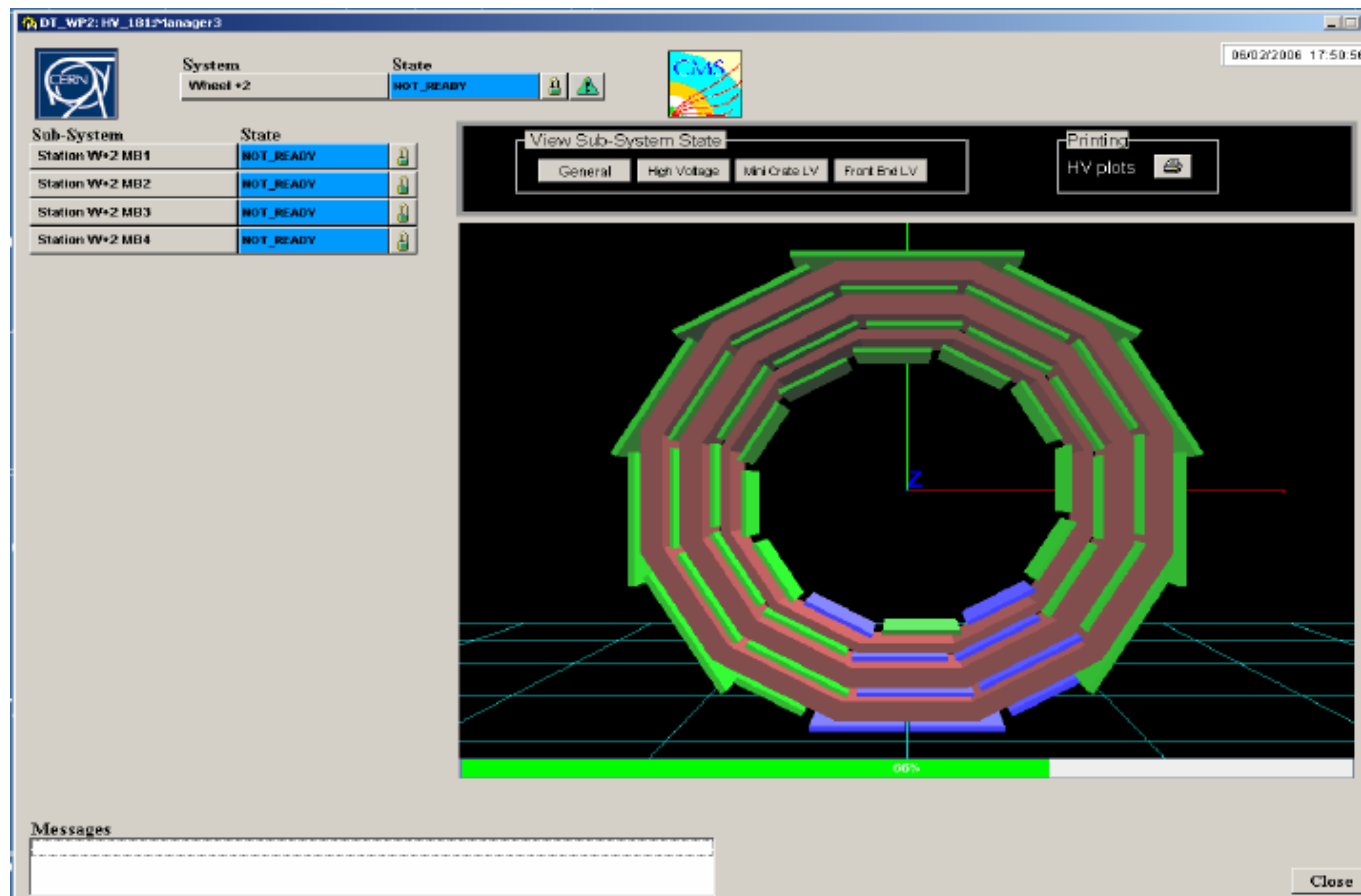
- HV
 - HV Boards: OK
 - Cables: ready and in position
 - Patch Panels: ready and in racks
- LV
 - LV Boards:
 - Finally received all boards from CAEN !!!
 - 3 A3009 boards on loan from RPC
 - Cables: ready !!
 - Patch Panels: almost ready

Controls

- HV and LV will be controlled using PVSS
 - prototype PVSS project already running at SX5
 - used to control HV & LV of installed chambers
 - chambers kept under HV since >3 month, LV controlled during sector test: no problems
- Now project running on 1 PC, but for MTCC we will use our 2 rack mounted PCs:
 - 1 for the HV
 - 1 for LV + Supervisor (including connection to MiniCrate state through PSI)

Included functionalities (I)

- Monitoring, settings, FsMs trees,...
- Java Interface (R. Gomez-Reino)



Included functionalities (II)

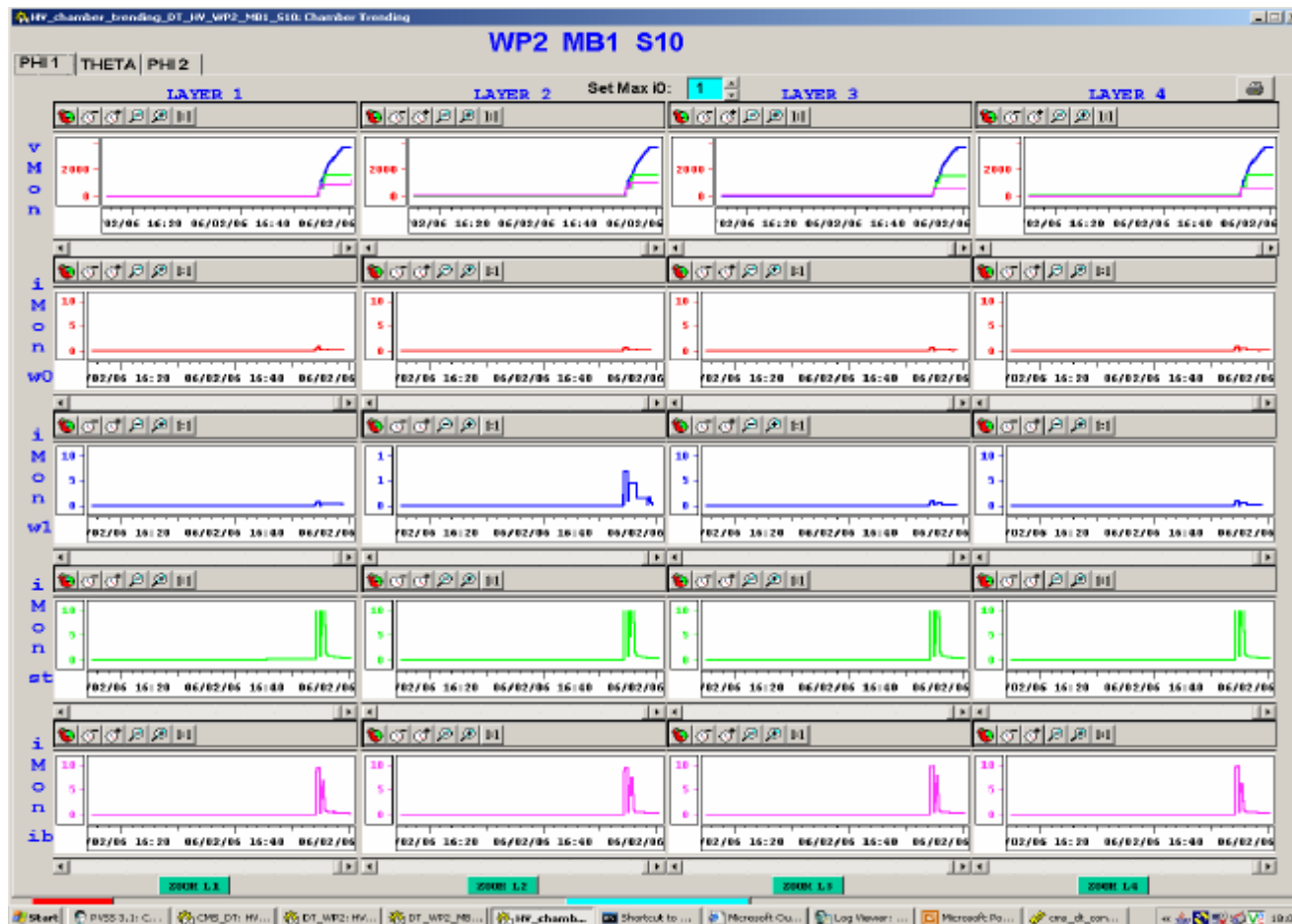
- Connection to Oracle Configuration Db

The screenshot displays the CMS DT HV Manager3 interface. At the top, the system status is shown as 'NOT_READY'. Below this, the sub-system 'Wheel +2' is also in a 'NOT_READY' state. The interface includes 'HV Settings' and 'LV Settings' sections, both currently set to 'standard'. A table lists various parameters and their settings, including voltage and current values for different beam and strip states. A small dialog box in the foreground displays the message 'Configuration Saved In Cache'. The interface also features a 'Messages' section at the bottom left and a 'Close' button at the bottom right.

Syst...	Channel Type	Parameter	Setting	Unit
HV	beam Normal State	v Max	1200	V
HV	beam Ramping Standby1	v0	800	V
HV	beam Ramping Standby1	i0	10	uA
HV	beam Ramping Standby1	v Max	1200	V
HV	beam Ramping On	v0	1200	V
HV	beam Ramping On	i0	10	uA
HV	beam Ramping On	v Max	1200	V
HV	Strip Normal State	v0	1800	V
HV	Strip Normal State	i0	5	uA
HV	Strip Normal State	v Max	1800	V
HV	Strip			
HV	Strip			

Included functionalities (III)

- Connection to Oracle Condition DB



Conclusions

- For the magnet test we will power a total of 14 DTs in sector 10 of wheel YB+1 and sectors 10 and 11 of wheel YB+2.
- HV and LV systems will be the final ones manufactured by CAEN.
- PVSS project with FSM integrated with the central CMS DCS is available for both HV and LV systems. First prototype PVSS project running since a few months at SX5.
- We have finally received all EASY modules. We also have final cables, final patch panels for HV and first prototype LV patch panels.