



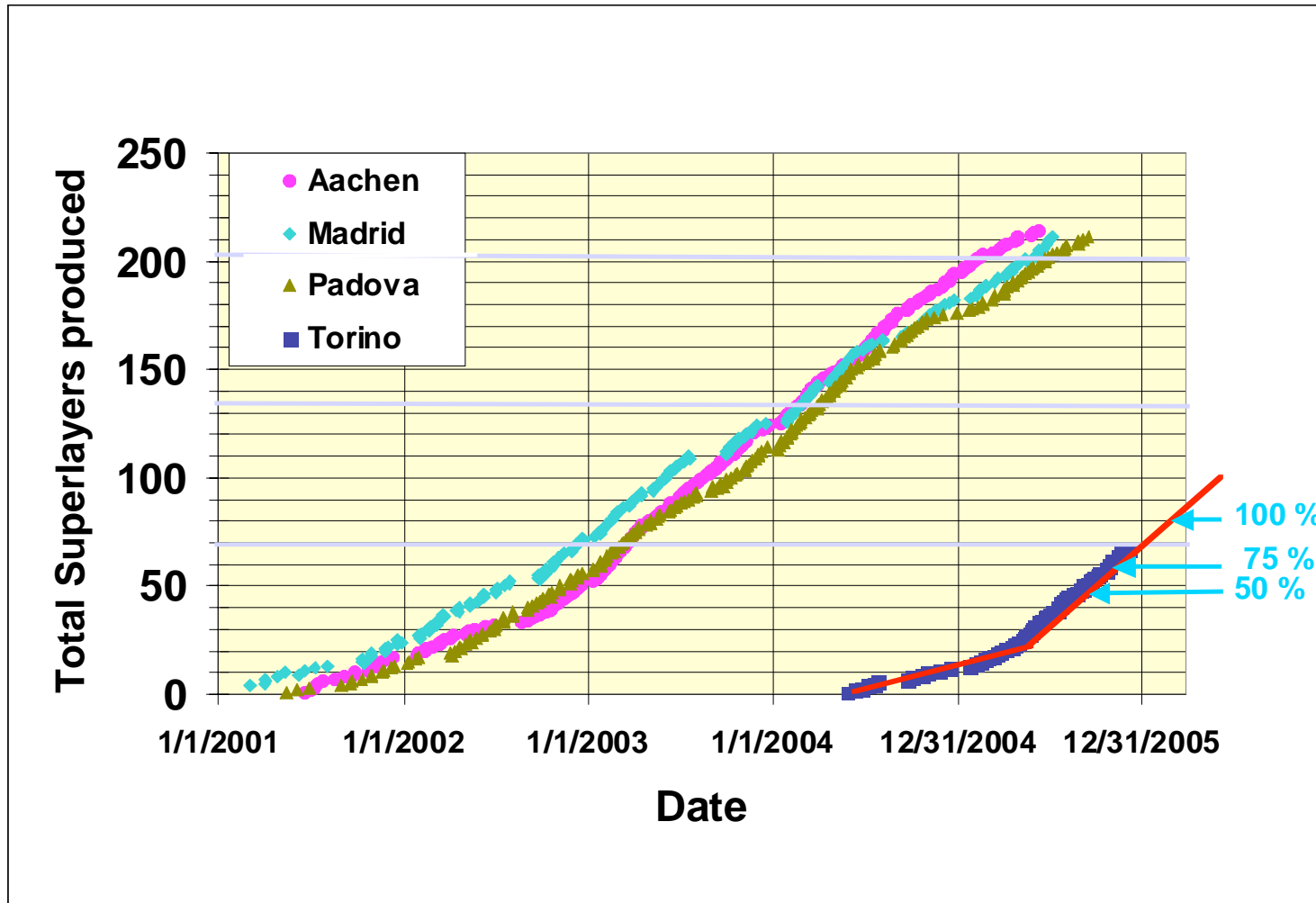
# Status of MB4 Production



1. Dashboard
2. Updates
3. DDU



# Status of MB4 Production

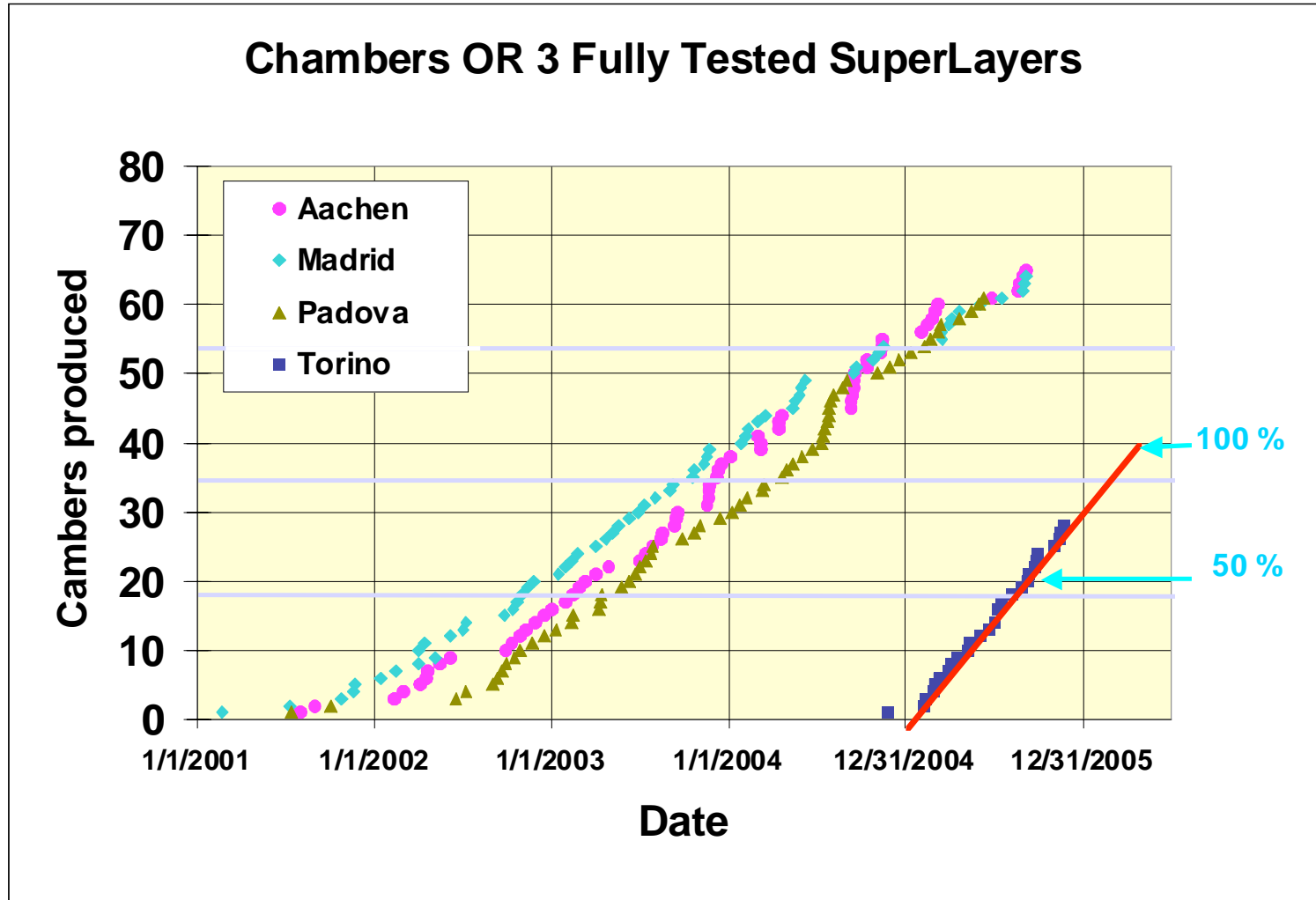




# Status of MB4 Production

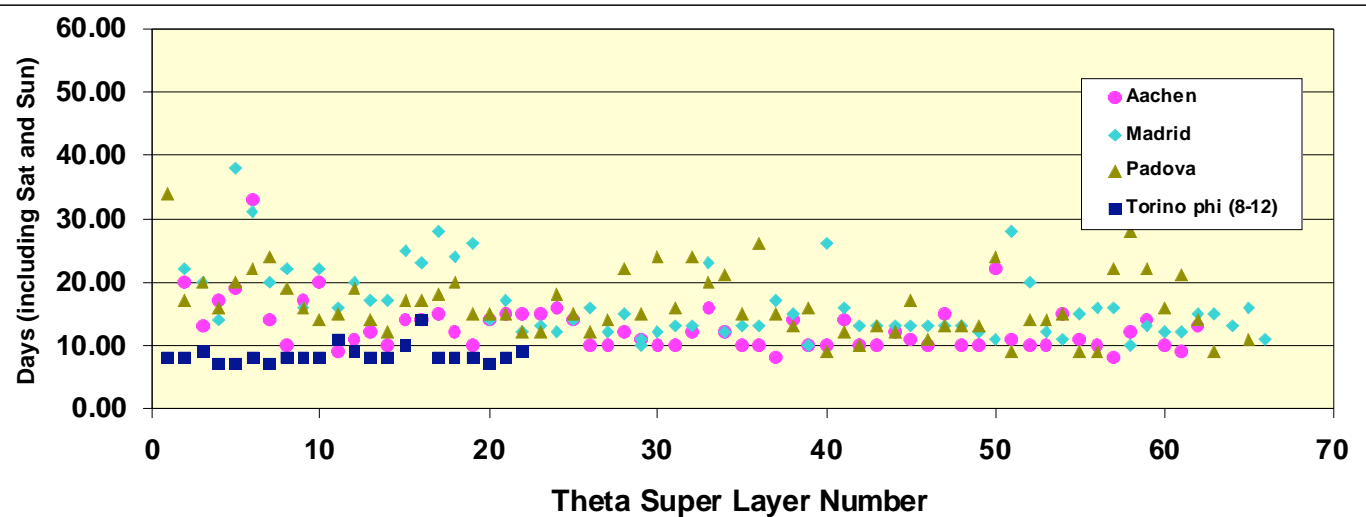
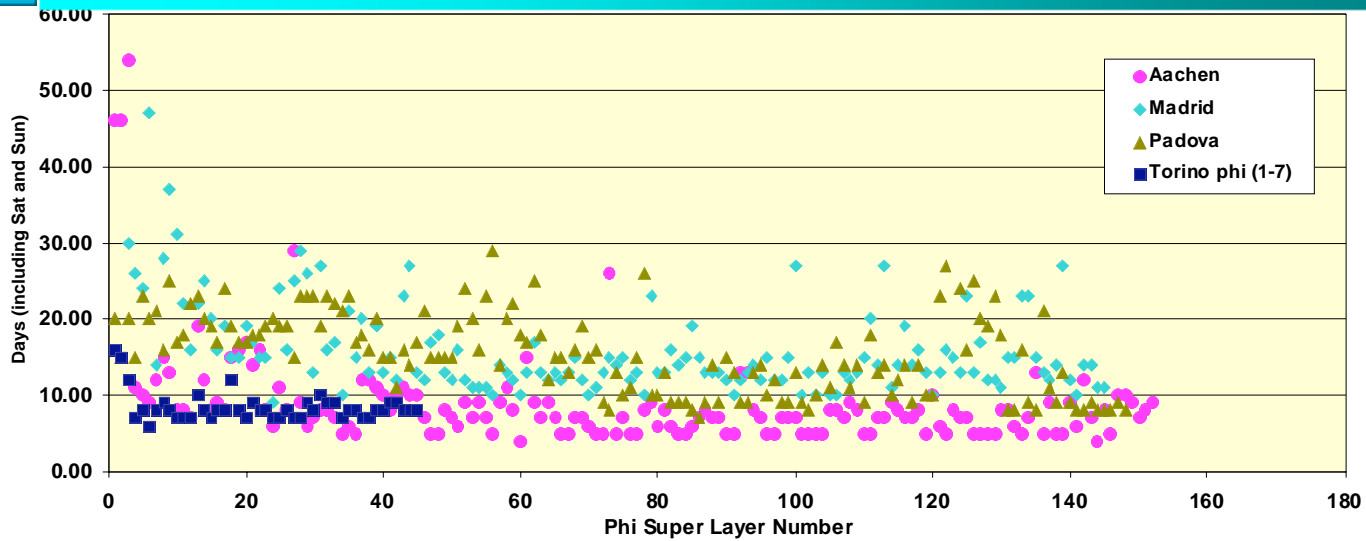


## Chambers OR 3 Fully Tested SuperLayers

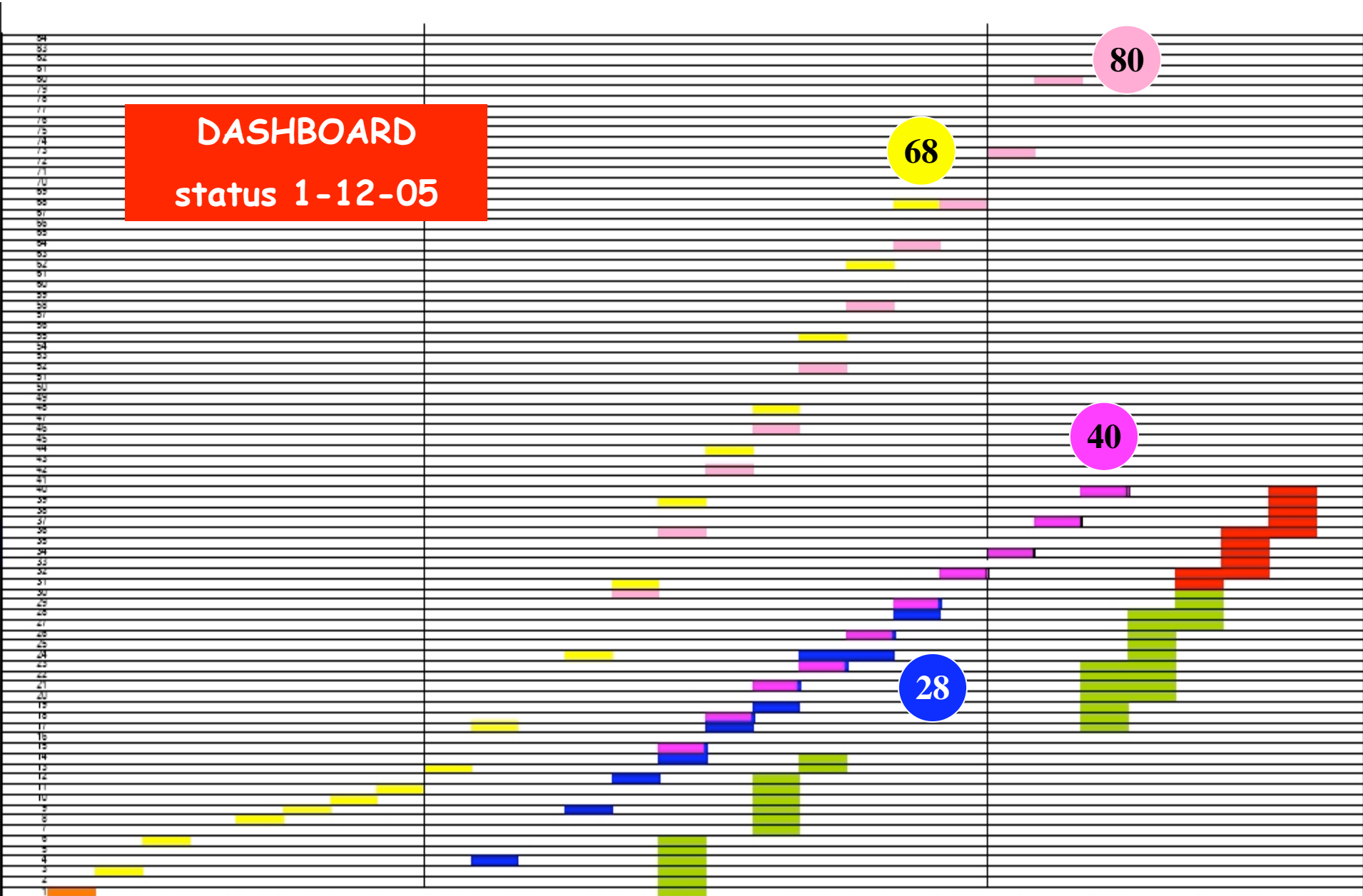




# Status of MB4 Production



**DASHBOARD**  
status 1-12-05



Slayers done
  planned
  Chambers done
  planned available at CERN
  installation window in SX
  installation windows in UX



## Status of MB4 Production



<b>SL Mechanically assembled</b>	<b>68 (85%)</b>
<b>SL Tested</b>	<b>62(77%)</b>
<b>Chambers assembled</b>	<b>28 (70%)</b>
<b>Chambers tested</b>	<b>27</b>
<b>Chambers at CERN</b>	<b>27</b>



## Status of MB4 Production



### No major problems:

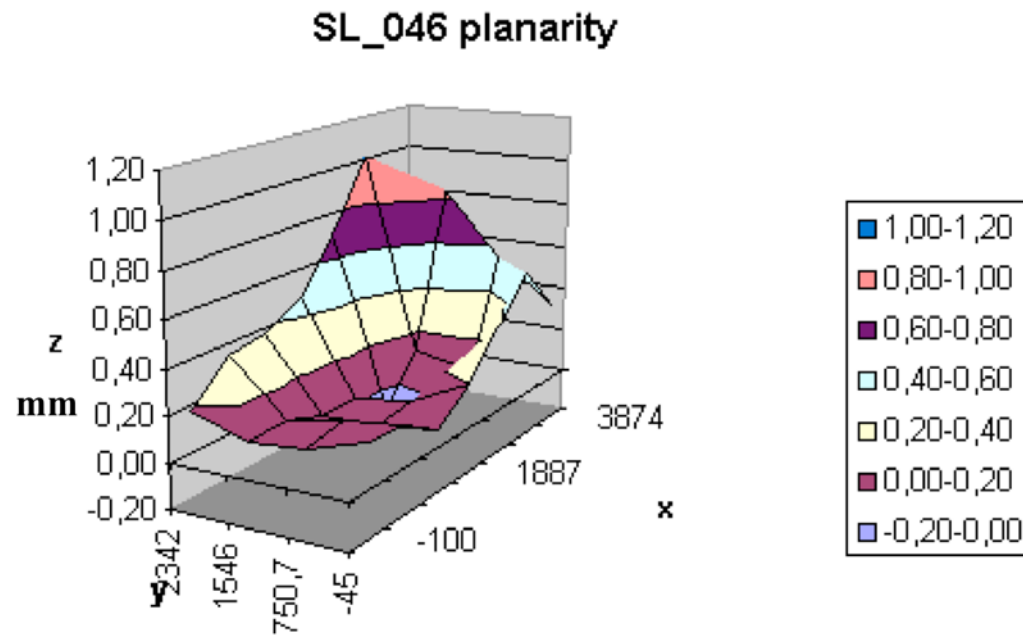
- One MB4(1,7) HC panel with very bad planarity ( $\sim 2\text{mm}$ ) and wavy surface, put aside; needs lot of care and detailed measurements to be used
- SL(1,7)\_046: last plate glued with a spacer left in the wrong place by the operator. Result, bad planarity and plate removed.



# Status of MB4 Production



SL planarity out of tolerance, plate peeled off.





## DT DDU/FED

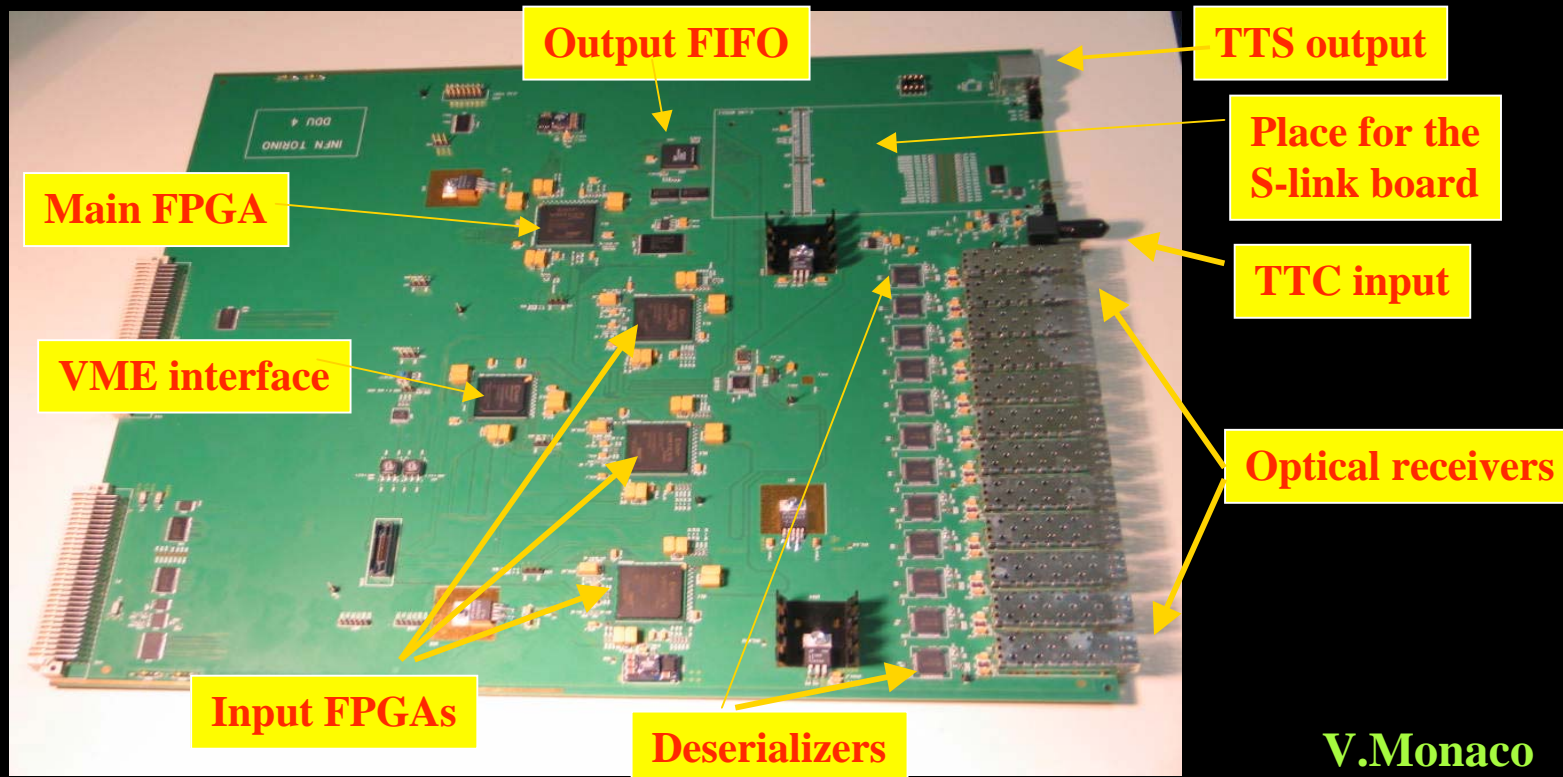
2 boards arrived. One board under test/debug:

Test and optimization of HW ongoing (few HW problems solved):

- Power ON (OK)
- VME interface (tested OK)
- clock distribution (tested OK jitter < 20 ps)
- FPGA loading (tested OK)

Working on:

- boundary scan software
- FPGA code debugging (data flow)
- TTC and TTS interface



V.Monaco