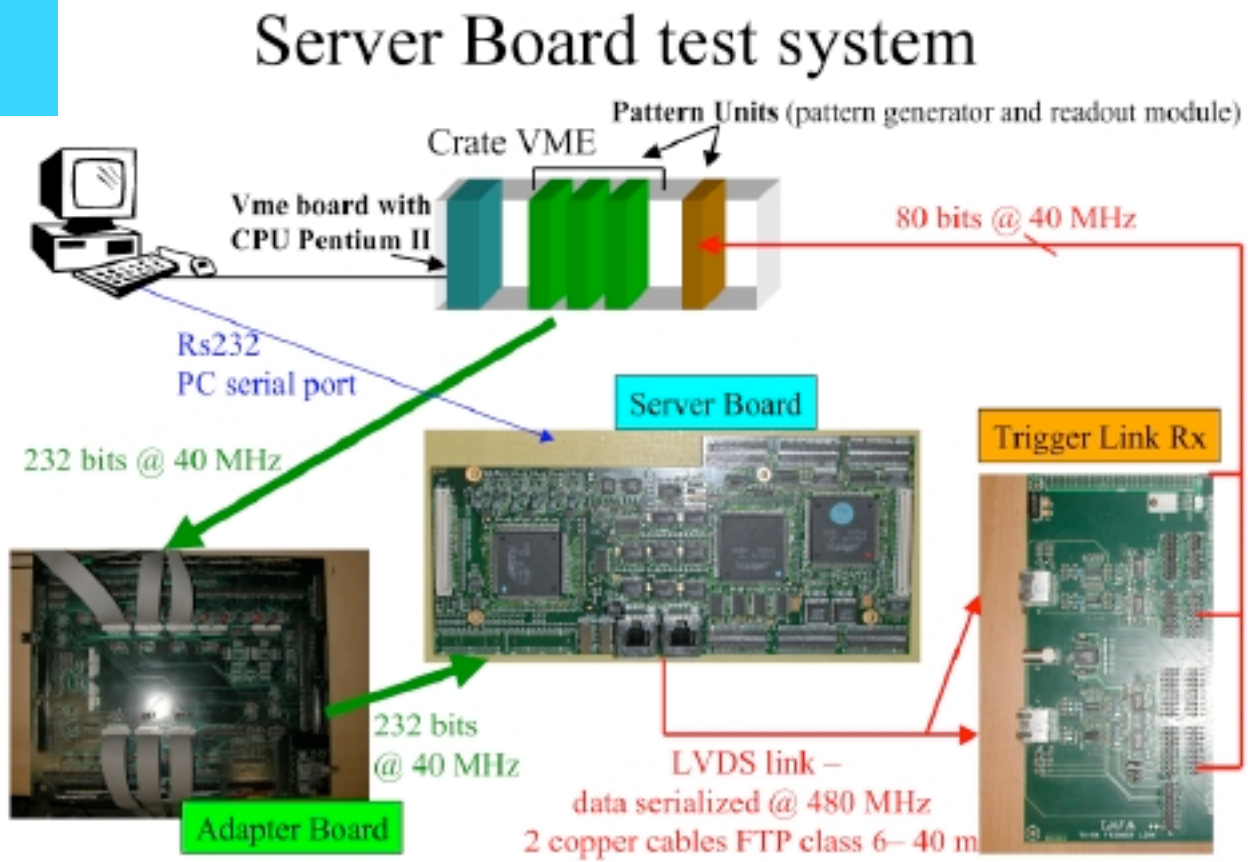
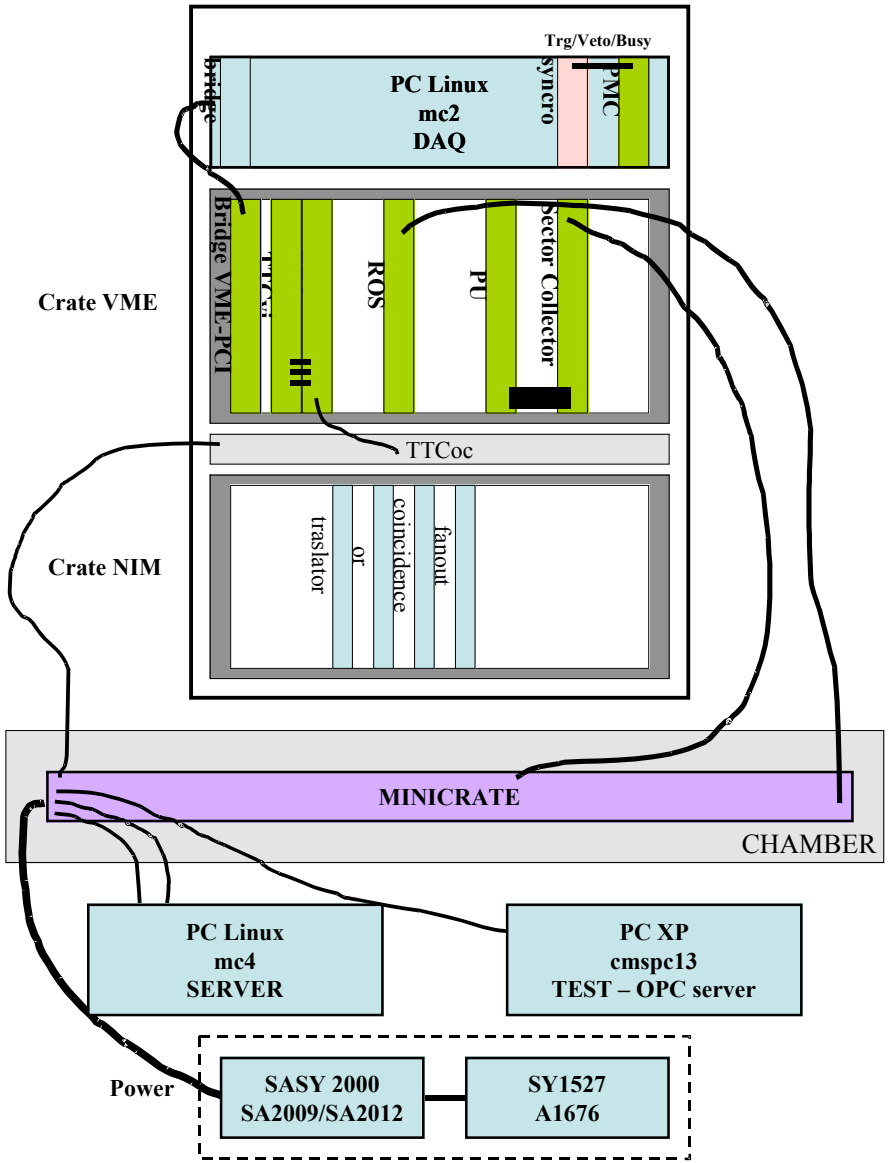
MC test setup: what we have

1.) test benches for boards



2.) Legnaro test-beam-like setup

2.) Legnaro test setup



(F.Gonella)

MC tests: what we have to do

tests **on the bench** for **internal connectivity** & integrity (RO Madrid, trigger Legnaro/Bologna). For trigger:

- power up SB/CCB, current drawing, communication with microc. on all 3 serial ports, check JTAG and Parallel Interface and control (resets...) signals on each board
- boundary scan of boards interconnections
- integrity of clock, chips configurability,run trigger emulation with test patterns and check output at the Sect.Coll.

Test connection **FE-RO-TRIG** (Legnaro)

- connect to FE of (dummy) test chamber and run test pulser
- run the full system trigger+DAQ+TTC..., adjust clock phase TRB-TRB and RO-TRB, vary thresholds

Status

Hardware tools for MC tests are/will shortly be available in several copies. We have only one dummy-chamber FE jig.
A complete system is setup and running in Legnaro.
We envisage two MC assembly and test sites (Legnaro,Bologna), a third test station setup at CERN. Target production of 16 MC/month might be possible.
First version of software for tests on the bench @ **end may**.
Start assembling trigger part of MC then.

Trigger parts now available for about **15 MC until september** (possible shortfall of TRBs then)

Test of the noise level when enlarging **FE cables holes**. If it is ok, larger holes will make easier the task of FE-MC cabling.